

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A down converter ~~(100)~~, comprising:
at least one switch;

an interface section, which connects the down converter to the
at least one switch; ~~(104)~~, and each switch is connected to a
respective

at least one driver circuit (102) is connected to the at least
one switch; and

a decoder connected to the at least one switch, wherein the
current at least one driver circuits circuit and the switches at
least one switch are disposed on a common integrated circuit (200),
and wherein the decoder is connected to a signal ground which is
different from a power ground of the common integrated circuit.

2. (Currently Amended) A The down converter as recited in claim 1, wherein ~~each of~~ the at least one driver circuit is a high-current driver circuit ~~(102)~~, and ~~each of~~ the at least ~~switches~~ switch is a power switch ~~(104)~~.

3. (Currently Amended) A The down converter as recited in claim 1, wherein the interface section is monolithically integrated with the at least one ~~power~~ switch.

4. (Currently Amended) A The down converter as recited in claim 1, wherein the down converter includes a high-side current driver ~~(102)~~, which is connected to a high-side power switch ~~(104)~~.

5. (Currently Amended) A The down converter as recited in claim 1, wherein the down converter includes a low-side current driver ~~(103)~~, which is connected to a low-side power switch ~~(105)~~.

6. (Currently Amended) A The down converter as recited in claim 4, wherein the high side current driver is connected to a level shifter ~~(102)~~.

7. (Currently Amended) A-The down converter as recited in claim 5, wherein a-the decoder ~~(106)~~ is connected to the low side block-current driver and to a level shifter.

8. (Currently Amended) A-The down converter as recited in claim 4, wherein the high-side current driver is a CMOS device ~~(202, 203)~~.

9. (Currently Amended) A-The down converter as recited in claim 5, wherein the low-side current driver is a CMOS device ~~(214, 215)~~.

10. (Currently Amended) A-The down converter as recited in claim 1, wherein a parasitic inductor between a-current-the at least one driver circuit and a-the at least one switch is ~~on the order of~~ approximately 1 nH or less.

11. (Currently Amended) A-The down converter as recited in claim 1, wherein the integrated circuit is a silicon-based

integrated circuit.

12. (Currently Amended) A The down converter as recited in claim 1, wherein the integrated circuit is a SiGe-based integrated circuit.